Refine Search

Search Results -

Terms	Documents
L1.clm.	12

Database:
US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search: L2

Refine Search

Recall Text | Clear | Interrupt

Search History

DATE: Thursday, August 31, 2006 Purge Queries | Printable Copy | Create Case

Set Name side by side	Query	Hit Count	Set Name result set
DB=PGPB; PLUR=YES; OP=OR			
L2	L1.clm.	12	L2
L1	((operating adj1 system adj1 domain$1) or OSD) same port same switch$3	43	L1

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
((operating adj1 system adj1 domain$1) or OSD) same port same switch$3	66

Database:
US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search: L1

Refine Search | Recall Text | Clear | Interrupt

Search History

DATE: Thursday, August 31, 2006 **Purge Queries** Printable Copy Create Case

Set Name side by side	Query	Hit Count	Set Name result set
DB=PGPB,USPT,USOC; PLUR=YES; OP=OR			
L1	((operating adj1 system adj1 domain$1) or OSD) same port same switch$3	66	L1

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
((operating adj1 system adj1 domain$1) or OSD) same port same switch$3	9

Database:
US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search: L2

Refine Search

Recall Text Clear Interrupt

Search History

DATE: Thursday, August 31, 2006 **Purge Queries** Printable Copy Create Case

Set Name side by side	Query	Hit Count	Set Name result set
DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR			
L2	((operating adj1 system adj1 domain$1) or OSD) same port same switch$3	9	L2
DB=PGPB,USPT,USOC; PLUR=YES; OP=OR			
L1	((operating adj1 system adj1 domain$1) or OSD) same port same switch$3	66	L1

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
(709/216 |709/220 |709/230 |709/250 |709/226 |370/351 |370/357 |370/362 |370/386 |370/399 |710/305 |710/313 |710/316 |710/317 |710/105 |710/33 |710/36 |714/4).ccls.	17110

Database:
- US Pre-Grant Publication Full-Text Database
- US Patents Full-Text Database
- US OCR Full-Text Database
- EPO Abstracts Database
- JPO Abstracts Database
- Derwent World Patents Index
- IBM Technical Disclosure Bulletins

Search: L4

Refine Search | Recall Text | Clear | Interrupt

Search History

DATE: Thursday, August 31, 2006 **Purge Queries** Printable Copy Create Case

Set Name side by side	Query
	DB=PGPB,USPT,USOC; PLUR=YES; OP=OR
L4	710/305,313,316,317,105,33,36;709/216,220,230,250,226;370/351,357,362,386,399;714/4.ccls.
L3	((operating adj1 system adj1 domain$1) or OSD) same port same switch$3
	DB=PGPB; PLUR=YES; OP=OR
L2	L1.clm.
L1	((operating adj1 system adj1 domain$1) or OSD) same port same switch$3

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
L1 and L4	7

Database:
US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search: L5

Refine Search | Recall Text | Clear | Interrupt

Search History

DATE: Thursday, August 31, 2006 Purge Queries Printable Copy Create Case

Set Name	Query
side by side	
	DB=PGPB,USPT,USOC; PLUR=YES; OP=OR
L5	l1 and L4
L4	710/305,313,316,317,105,33,36;709/216,220,230,250,226;370/351,357,362,386,399;714/4.ccls.
L3	((operating adj1 system adj1 domain$1) or OSD) same port same switch$3
	DB=PGPB; PLUR=YES; OP=OR
L2	L1.clm.
L1	((operating adj1 system adj1 domain$1) or OSD) same port same switch$3

END OF SEARCH HISTORY

Results for "(((operating system)<in>metadata) <and> (port<in>metadata))<and> (swi..." e-mail
Your search matched **10** of **1397873** documents.
A maximum of **100** results are displayed, **25** to a page, sorted by **Relevance** in **Descending** order.

» Search Options

View Session History

New Search

Modify Search

(((operating system)<in>metadata) <and> (port<in>metadata))<and> (switch*<in Search

Check to search only within this results set

Display Format: Citation Citation & Abstract

» Key

IEEE JNL	IEEE Journal or Magazine
IEE JNL	IEE Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IEE CNF	IEE Conference Proceeding
IEEE STD	IEEE Standard

view selected items Select All Deselect All

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EtheReal: a host-transparent real-time Fast Ethernet swit

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On page(s): 12 - 21
Number of Pages: xii+349
Meeting Date: 10/13/1998 - 10/16/1998
Location: Austin, TX
INSPEC Accession Number:6251244
Digital Object Identifier: 10.1109/ICNP.1998.723721
Posted online: 2002-08-06 21:51:45.0

Abstract
Distributed multimedia applications require guaranteed quality of service (QoS) from the
This paper describes the design, implementation, and evaluation of a real-time Fast Ethe
EtheReal that provides bandwidth guarantees to real-time applications running on Ethern
modification to the hardware and **operating system** on the host machines. At the heart c
architecture is a novel real-time connection setup protocol that is completely transparent
OS, and thus allows the **switch** to be deployed in a network of heterogeneous machines
platforms. The only dependency of EtheReal on the hosts is their support for TCP/UDP/I
EtheReal **switch** uses an Ethernet address swapping technique for real-time packets, sir
the complexity of maintaining a global connection ID space. Because of the inherent CR(
Ethernet hardware, this technique significantly reduces the total processing overhead cor
swapping at higher network layers. The current EtheReal **switch** prototype is fully operat
off-the-shelf PC hardware. It is capable of supporting up to 640 Mbits/sec across 4 **ports**
connection establishment overhead of 0.1-0.6 msec and a 10 µs non-real-time packet lat

Index Terms
Inspec
Controlled Indexing
electronic **switching** systems local area networks microcomputer applications
communication network **operating systems** packet **switching** quality of ser
systems telecommunication computing transport protocols

Non-controlled Indexing
0.1 to 0.6 ms 10 mus 640 Mbit/s CRC support EtheReal **switch** architecture
platforms QoS TCP/UDP/IP address swapping technique bandwidth guaran
distributed multimedia applications global connection ID space guaranteed qu
service heterogeneous machines higher network layers host-transparent **swi**
time packet latency off-the-shelf PC hardware **operating system** per-hop co
establishment overhead real-time Fast Ethernet **switch** real-time applications
connection setup protocol real-time packets total processing overhead reducti
Author Keywords
Not Available

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